

Kadono et al.

Serial No.: 09/535,233

Filed: March 24, 2000

For: Semiconductor Device And A

Method Of Manufacturing The Same

Art Unit: 2815

Examiner: M. Warren

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to:

Commissioner for Patents
Washington, D.C. 20231

CHRISTINE A BARGLIK

Church a Barglik June 8, 2001

Signature Date

Commissioner for Patents Washington, D.C. 20231

ELECTION AND AMENDMENT A

Sir:

In response to the Restriction Requirement of May 8, 2001, Applicants elect to prosecute Group II: Claims 11-14 in the above-identified application. Applicants are making this election without prejudice to later filing a divisional application on the non-elected claims.

Please also amend the above-identified application as follows:

IN THE CLAIMS:

Please cancel Claims 1-10.

Please add the following new claims:

06/14/2001 CCKAU1 00000112 09535233

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